



PROGRAMMABLE TIMING GENERATOR WITH OFFSET AND WIDTH CONTROL USING DELAY LOCK LOOP

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U.S. Serial No. 10/637,401 (D85269/PCW)

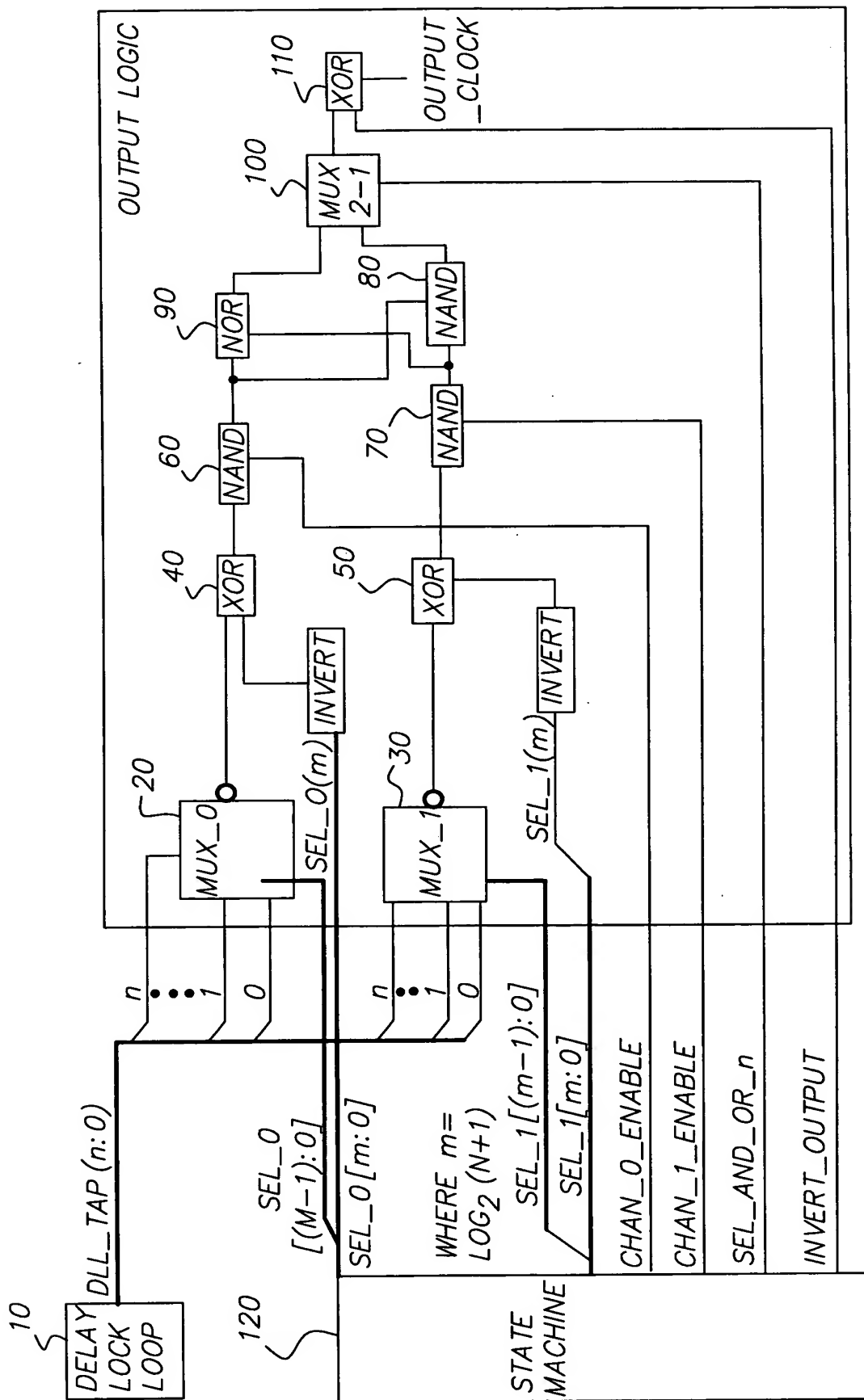
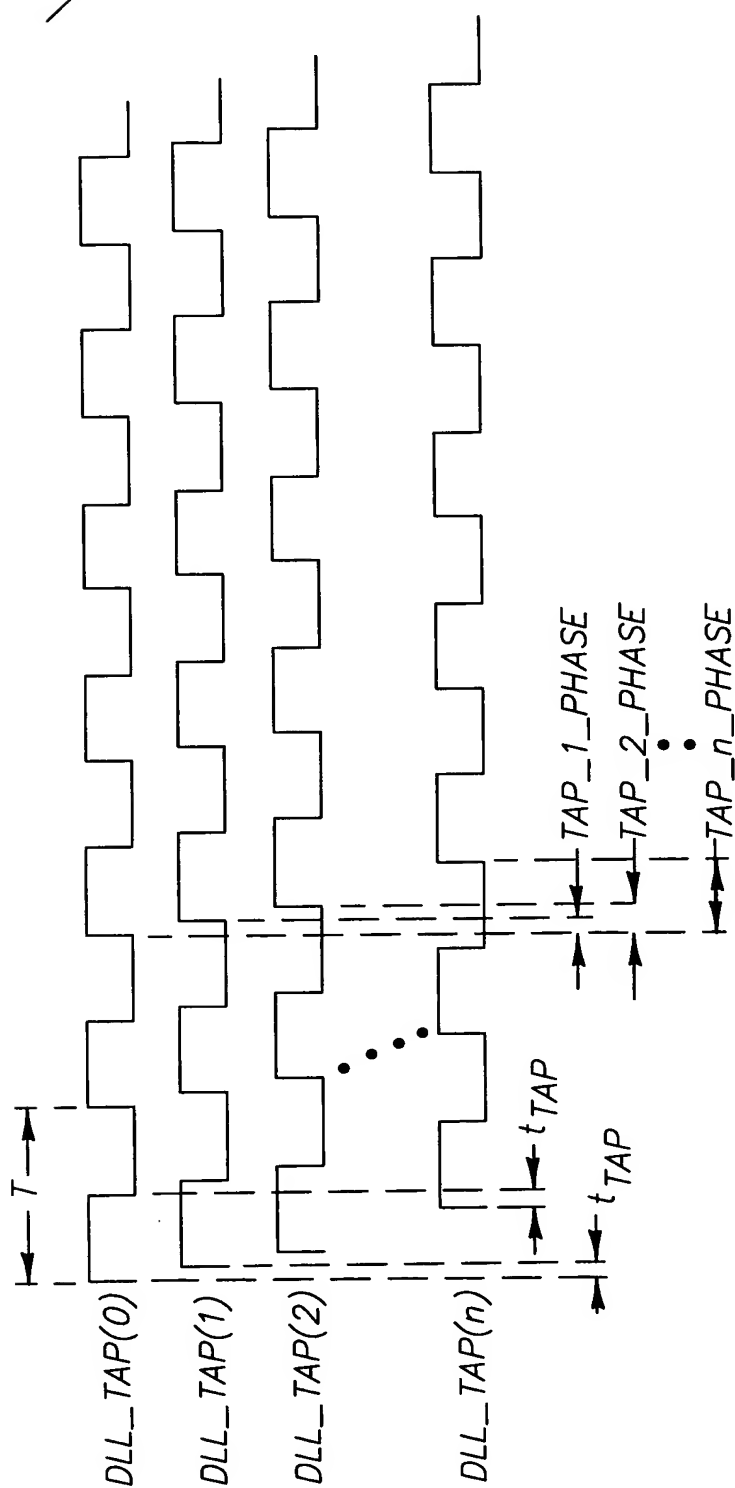


FIG. 1



$T_{tap} = (1/[(n+1)*T] = \text{WHERE } T = \text{CLOCK PERIOD, } n = \text{NUMBER OF DLL TAPS} = 1/2 \text{ THE}$

NUMBER OF OUTPUT CLOCK OFFSET/WIDTH POSITIONS WITHIN ONE PERIOD, T .

$TAP_0_PHASE = T_{TAP} * 0 = 0$

$TAP_1_PHASE = T_{TAP} * 1$

$TAP_2_PHASE = T_{TAP} * 2$

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$TAP_n_PHASE = T_{TAP} * n$

FIG. 2

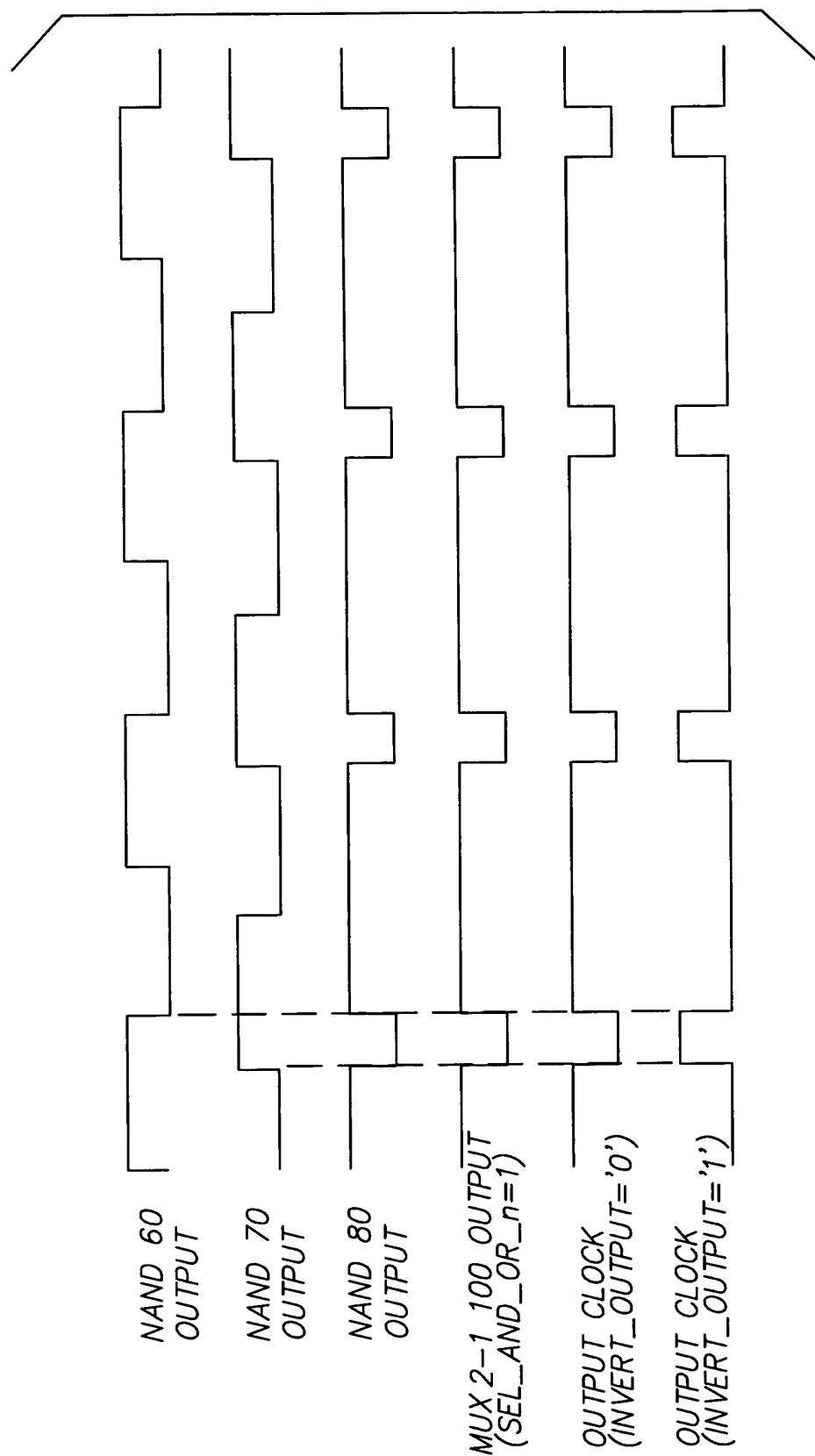


FIG. 3

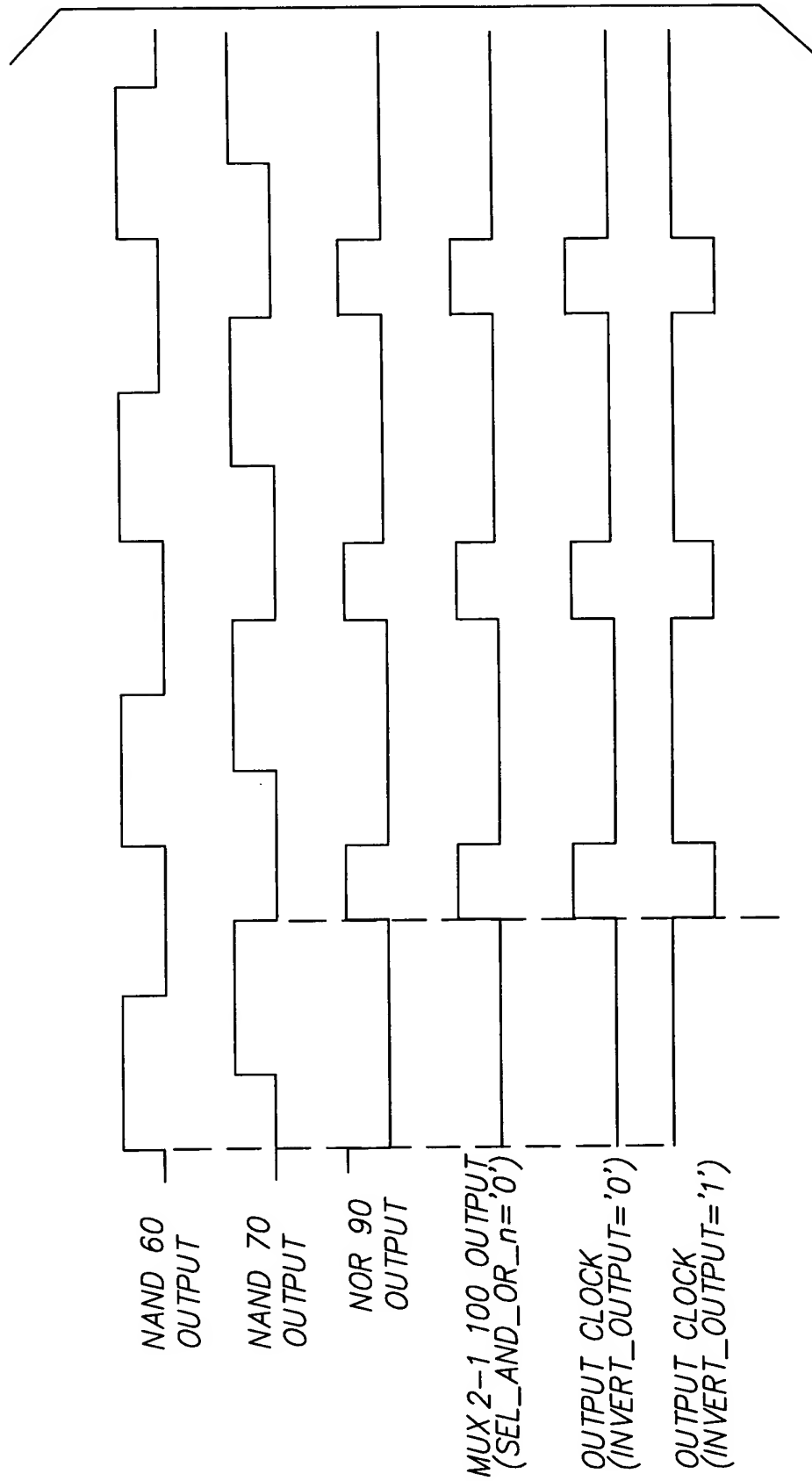


FIG. 4